

FIG. 1B (PRIOR ART)

FIG. 1A (PRIOR ART)

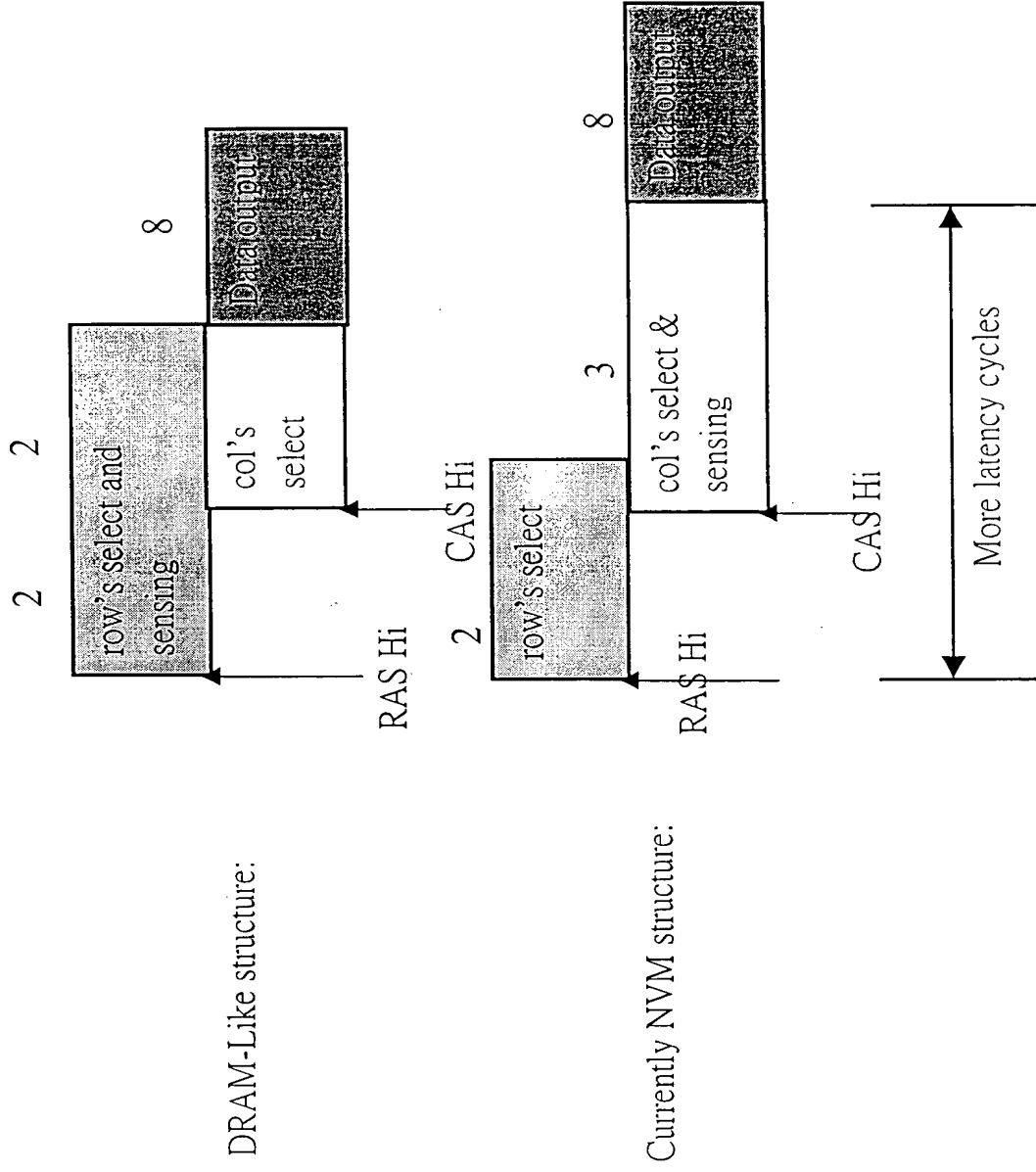


FIG. 2 (PRIOR ART)

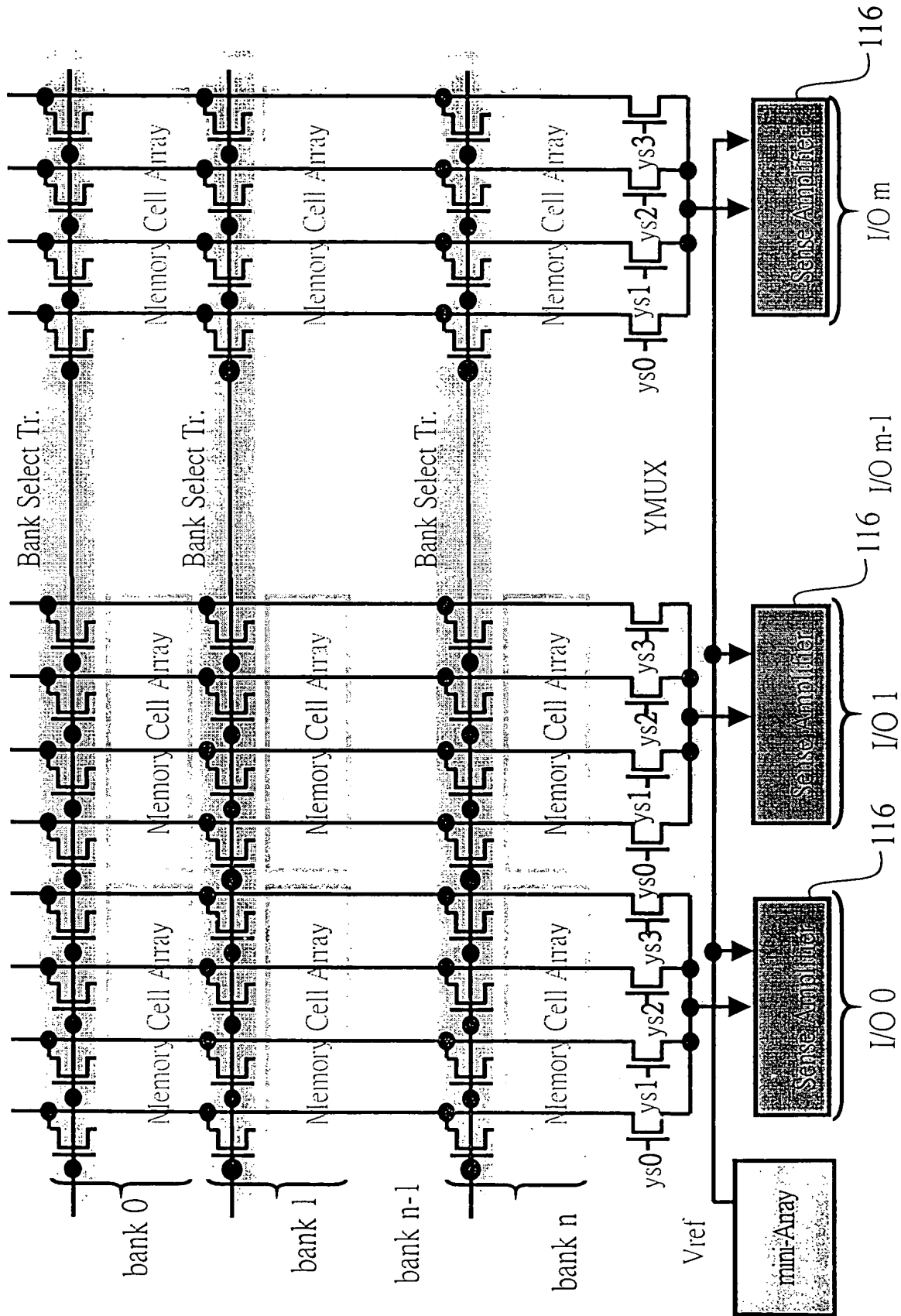


FIG. 3 (PRIOR ART)

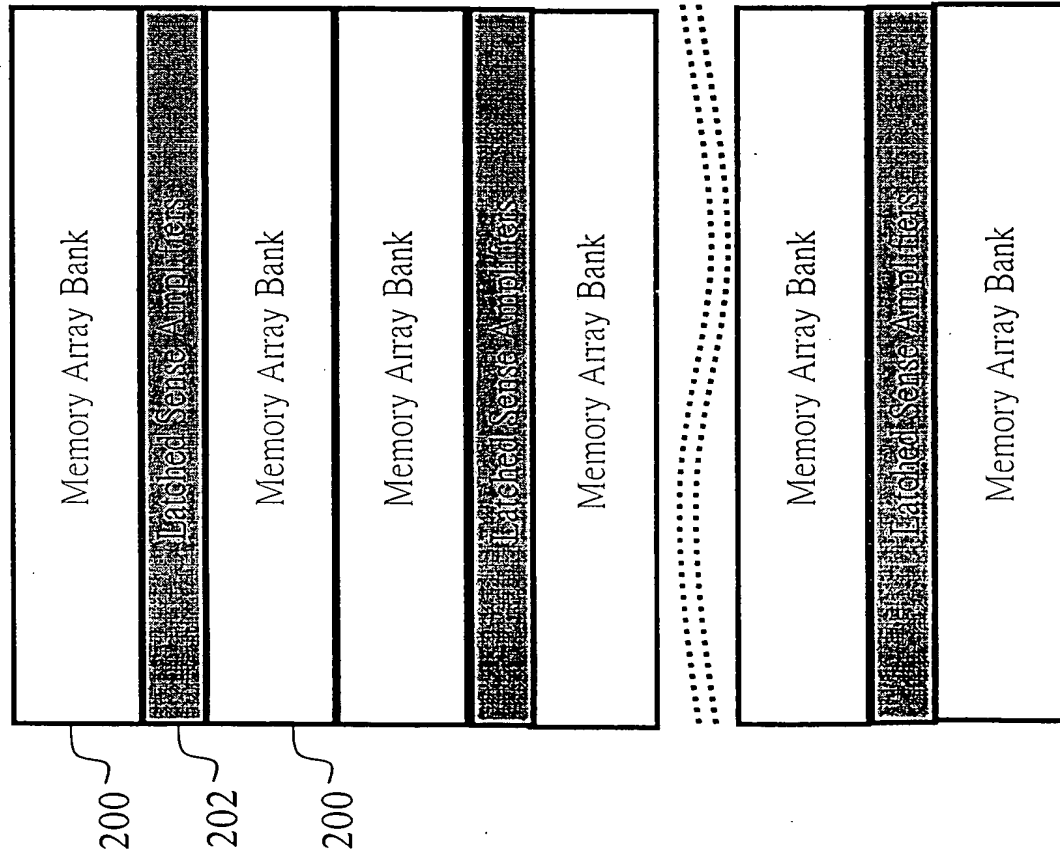


FIG. 4A

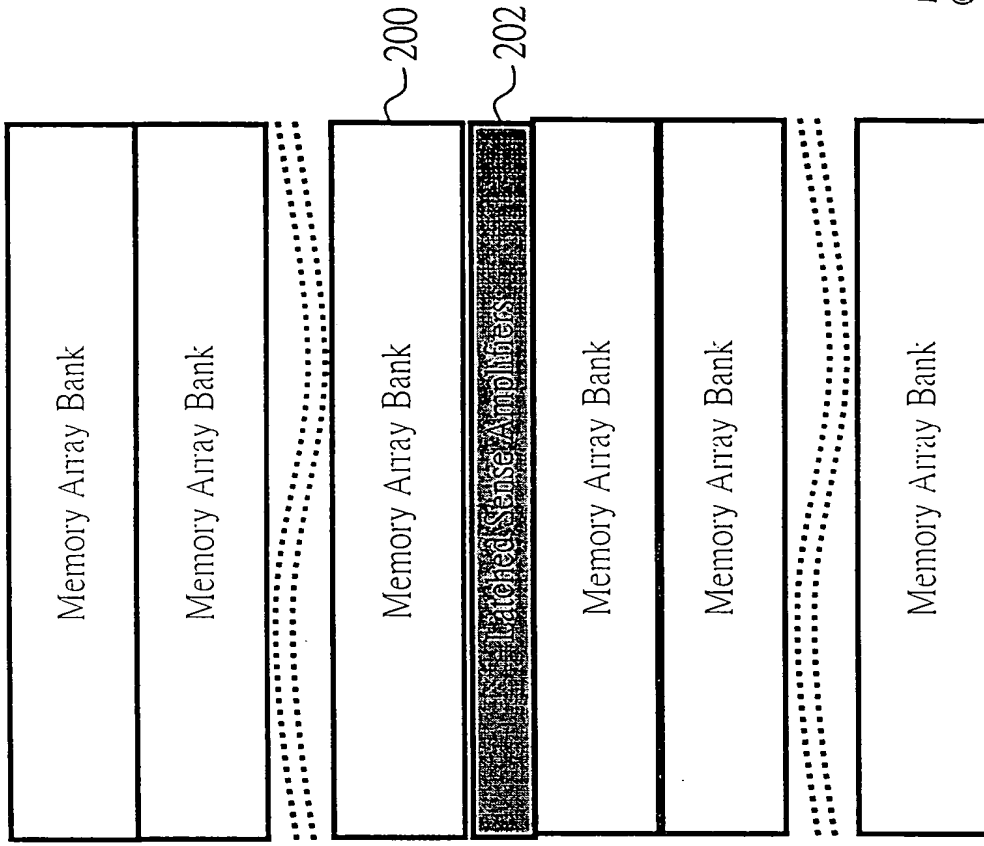


FIG. 4B

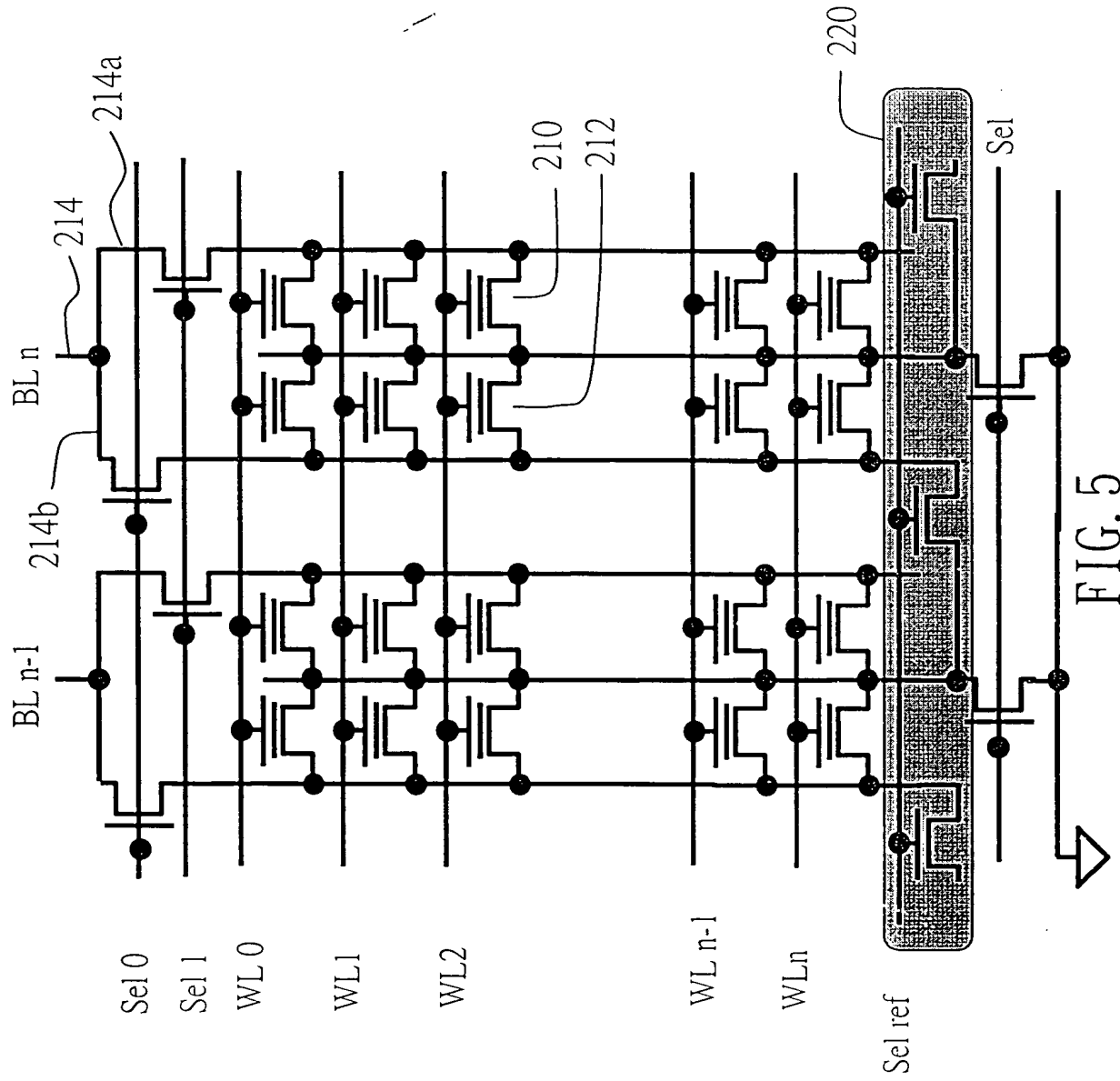


FIG. 5

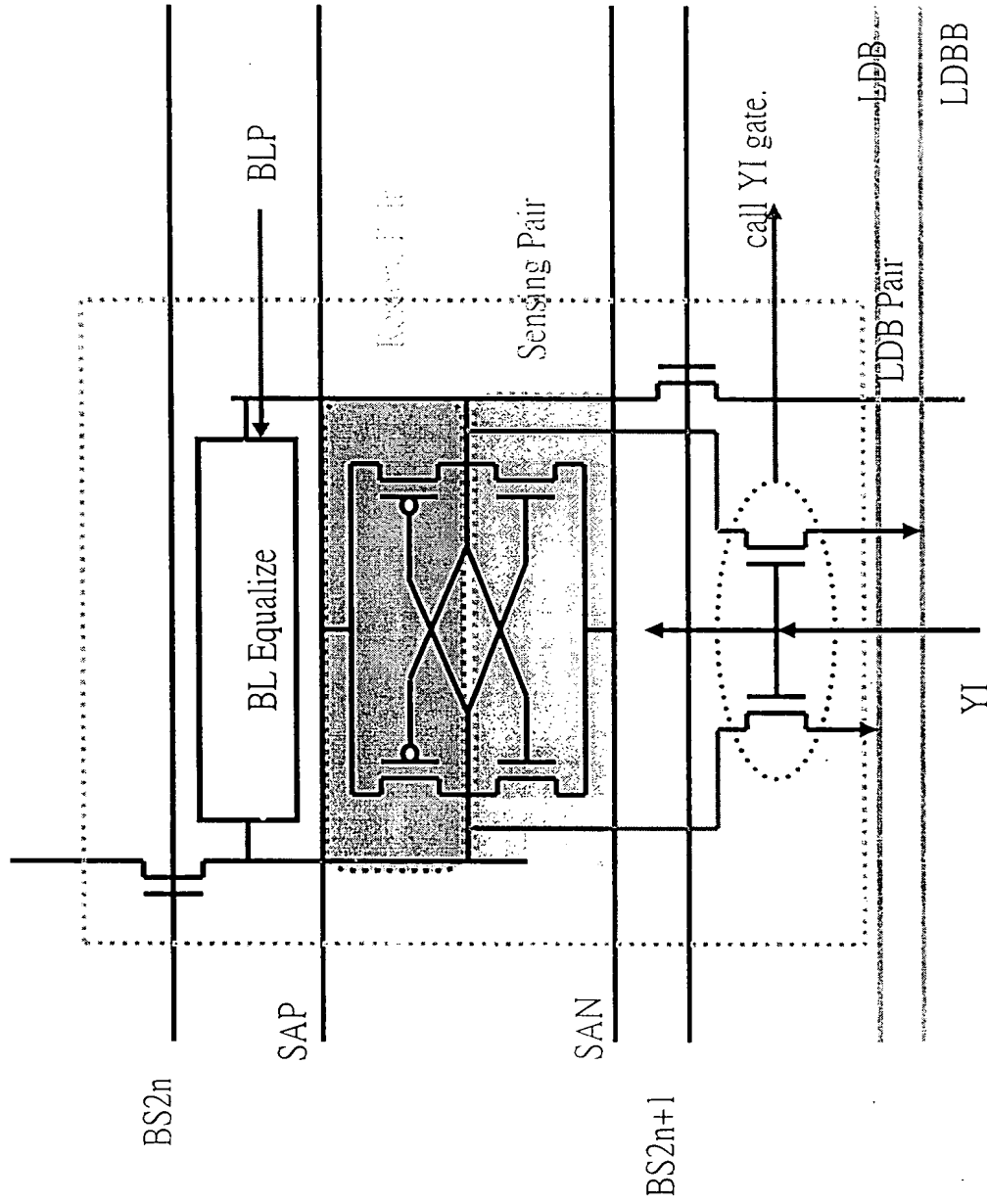


FIG. 6

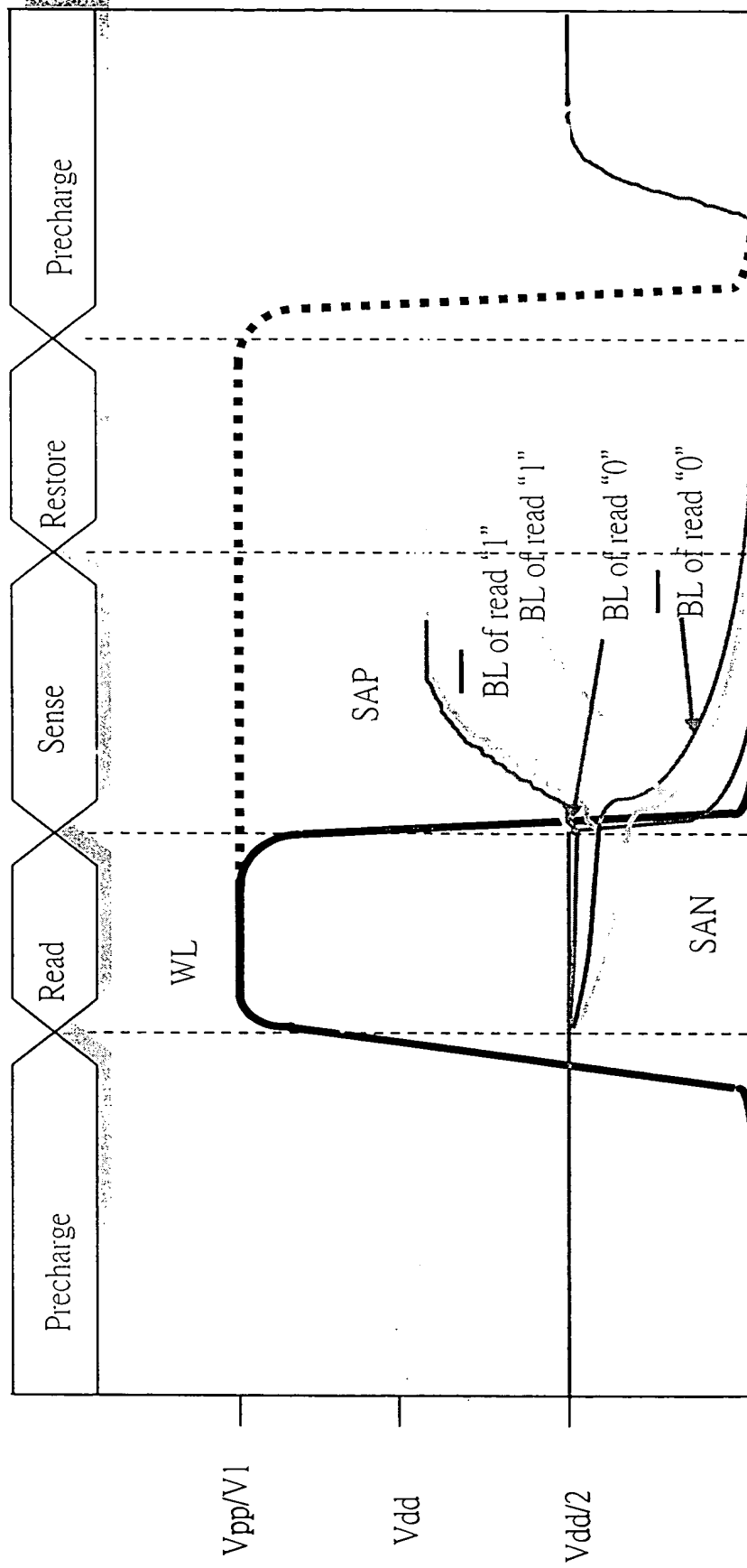


FIG. 7

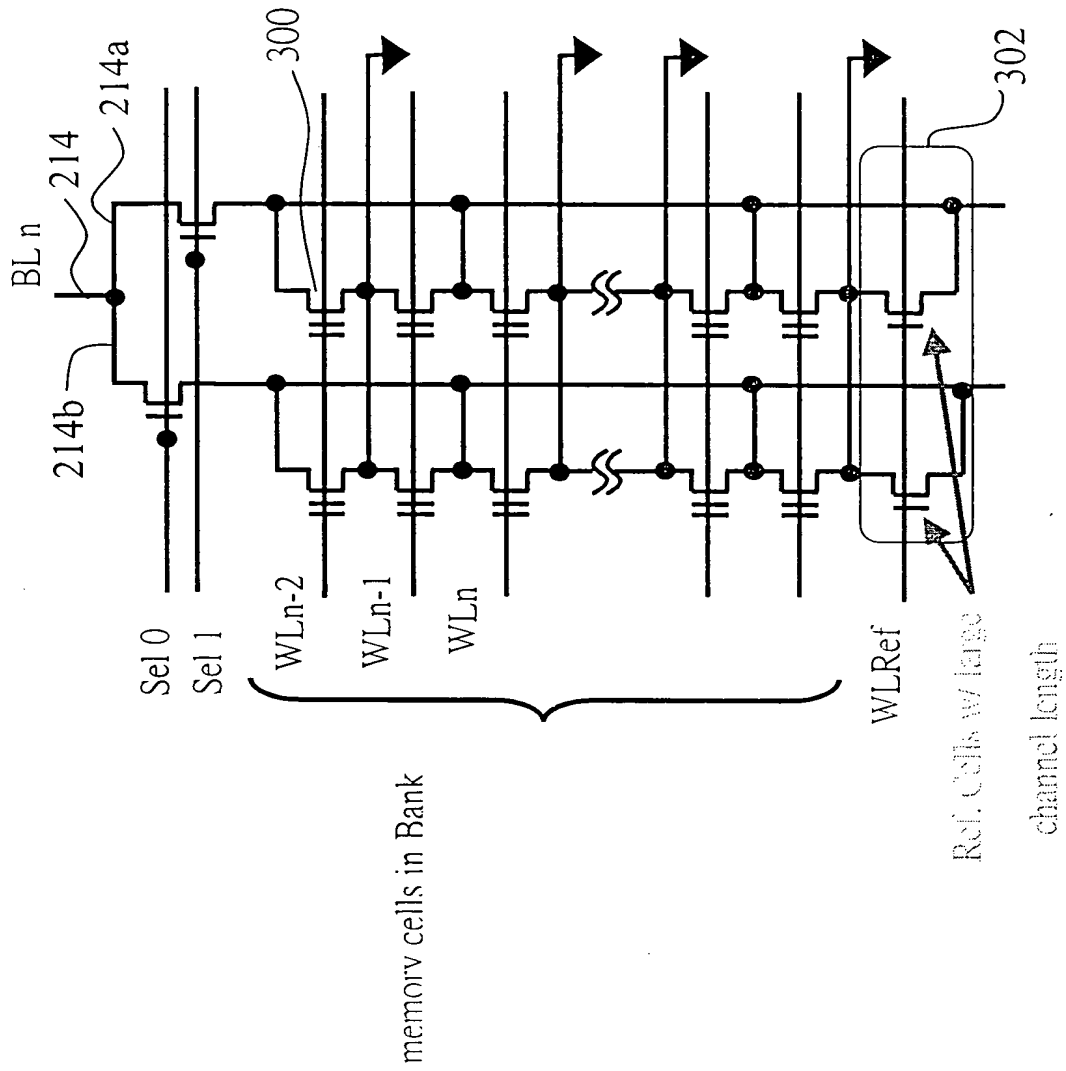


FIG. 8



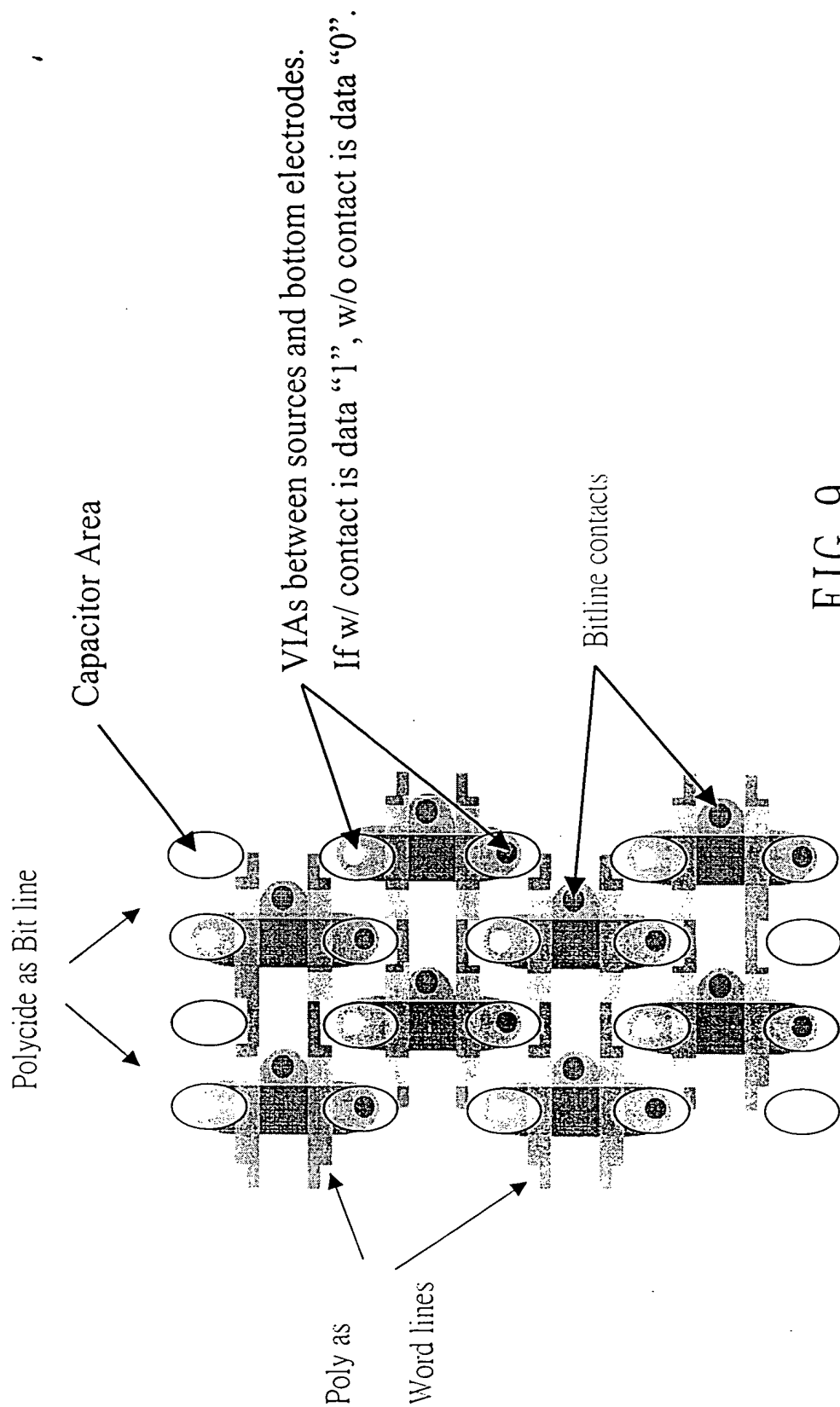
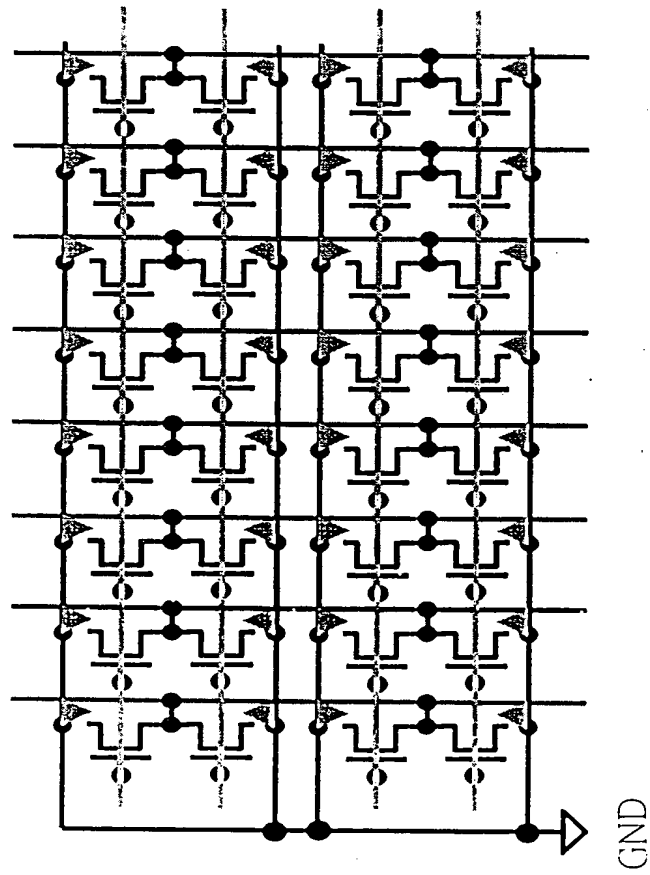


FIG. 9



▼ : the code transistor is connected to the GND line.

FIG. 10

folded bit line structure based on the mask ROM

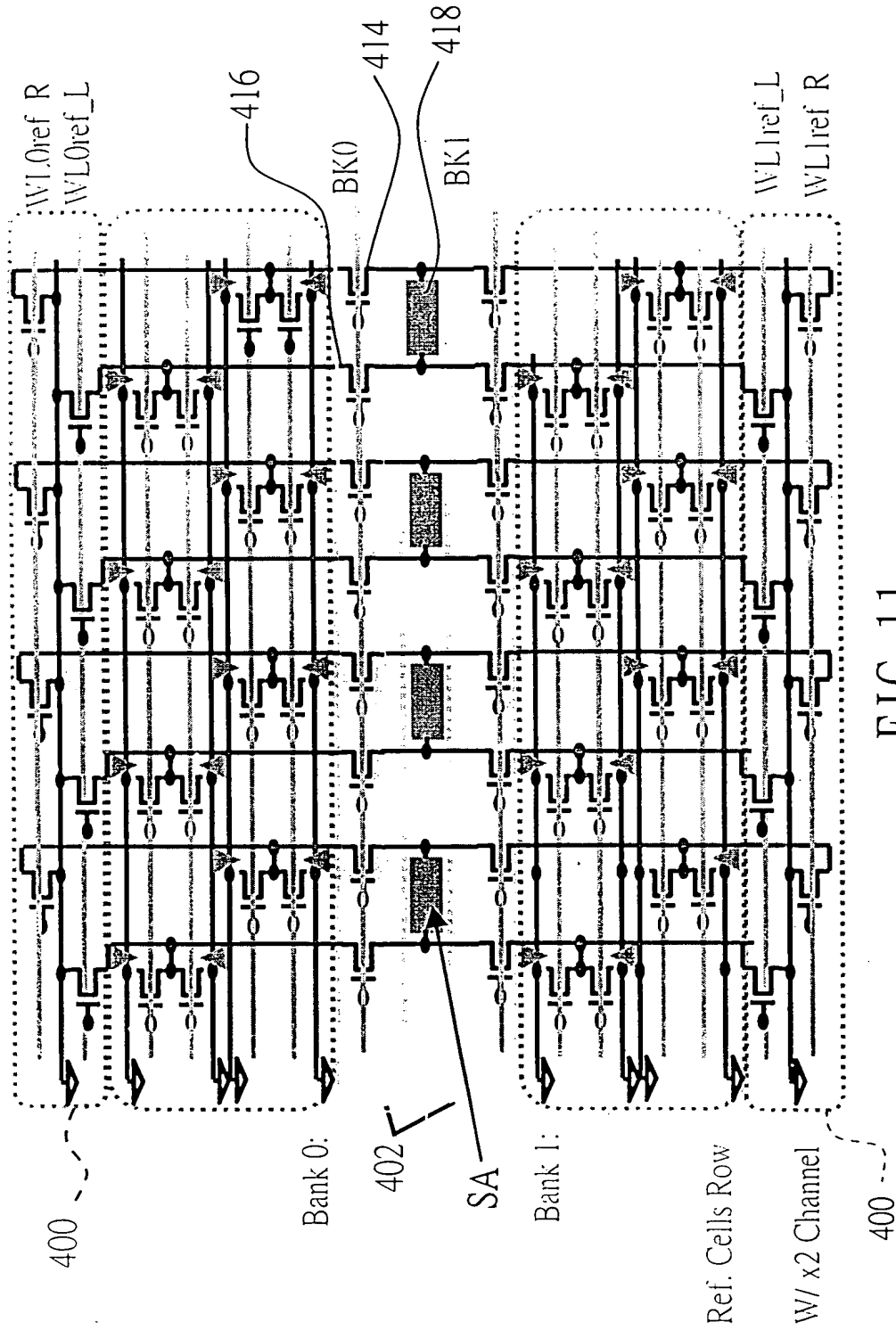


FIG. 11

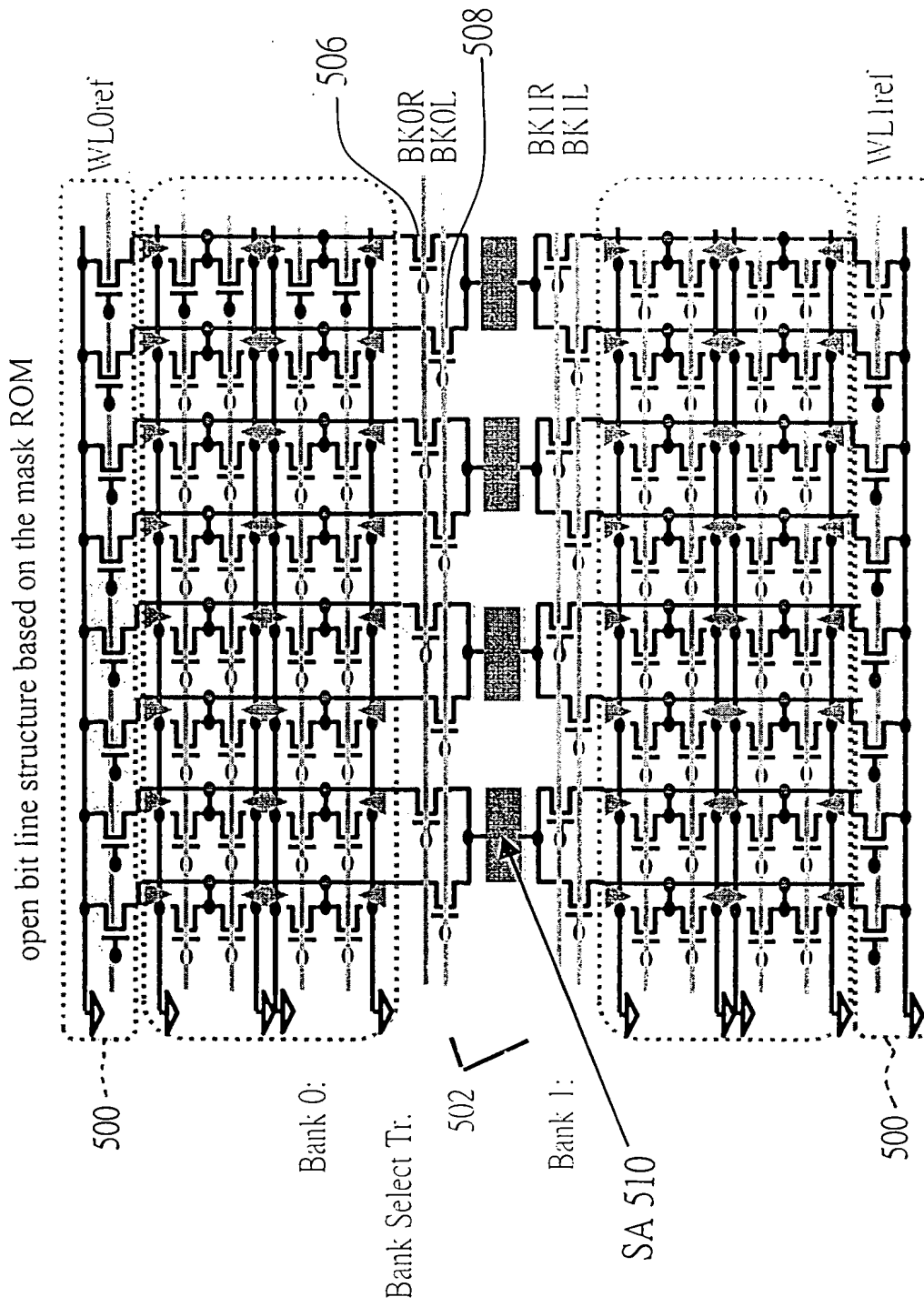


FIG. 12

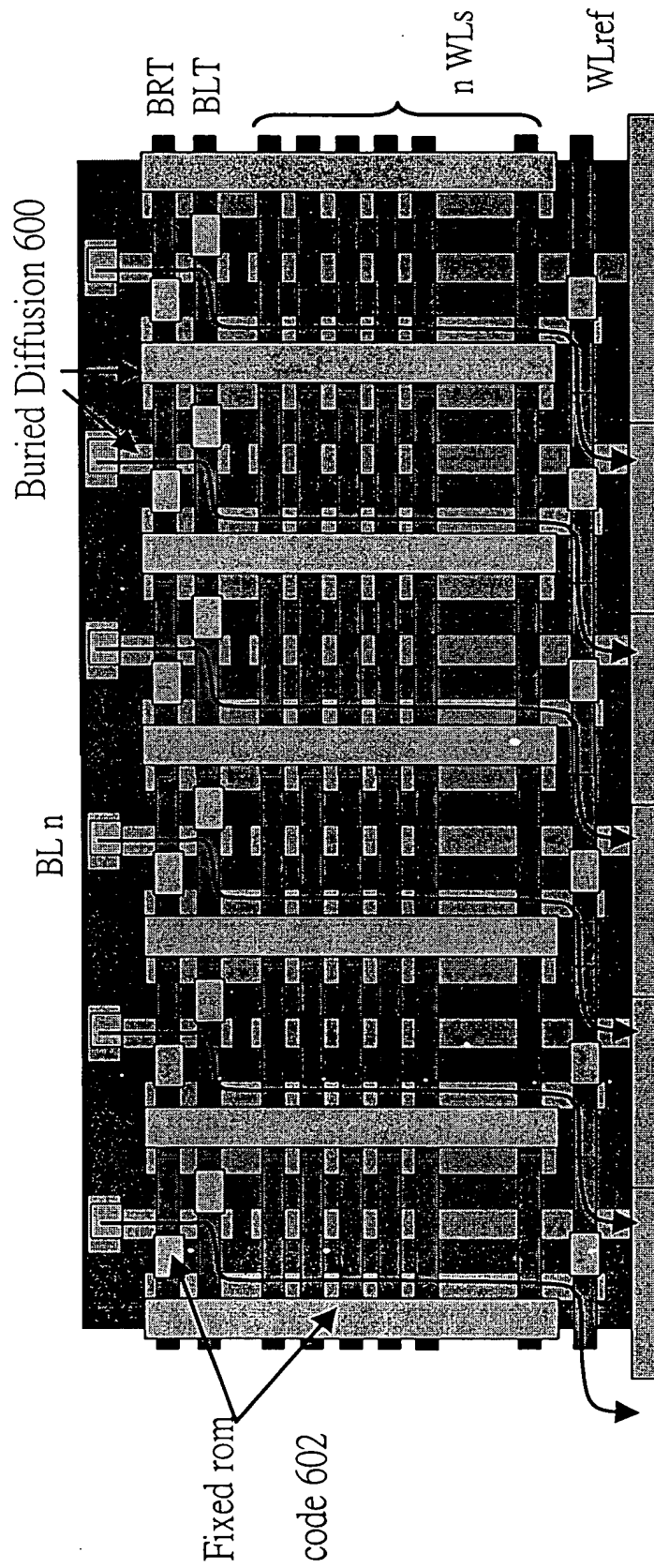


FIG. 13

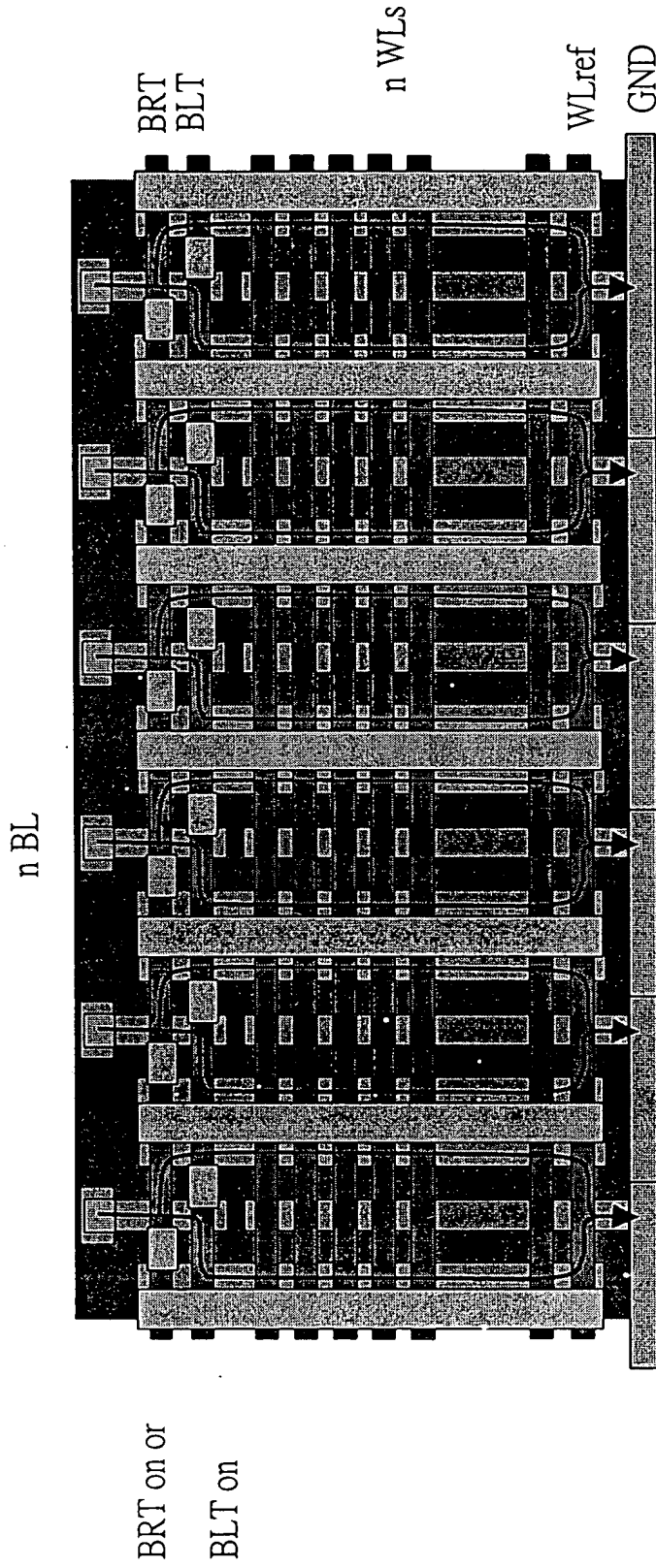
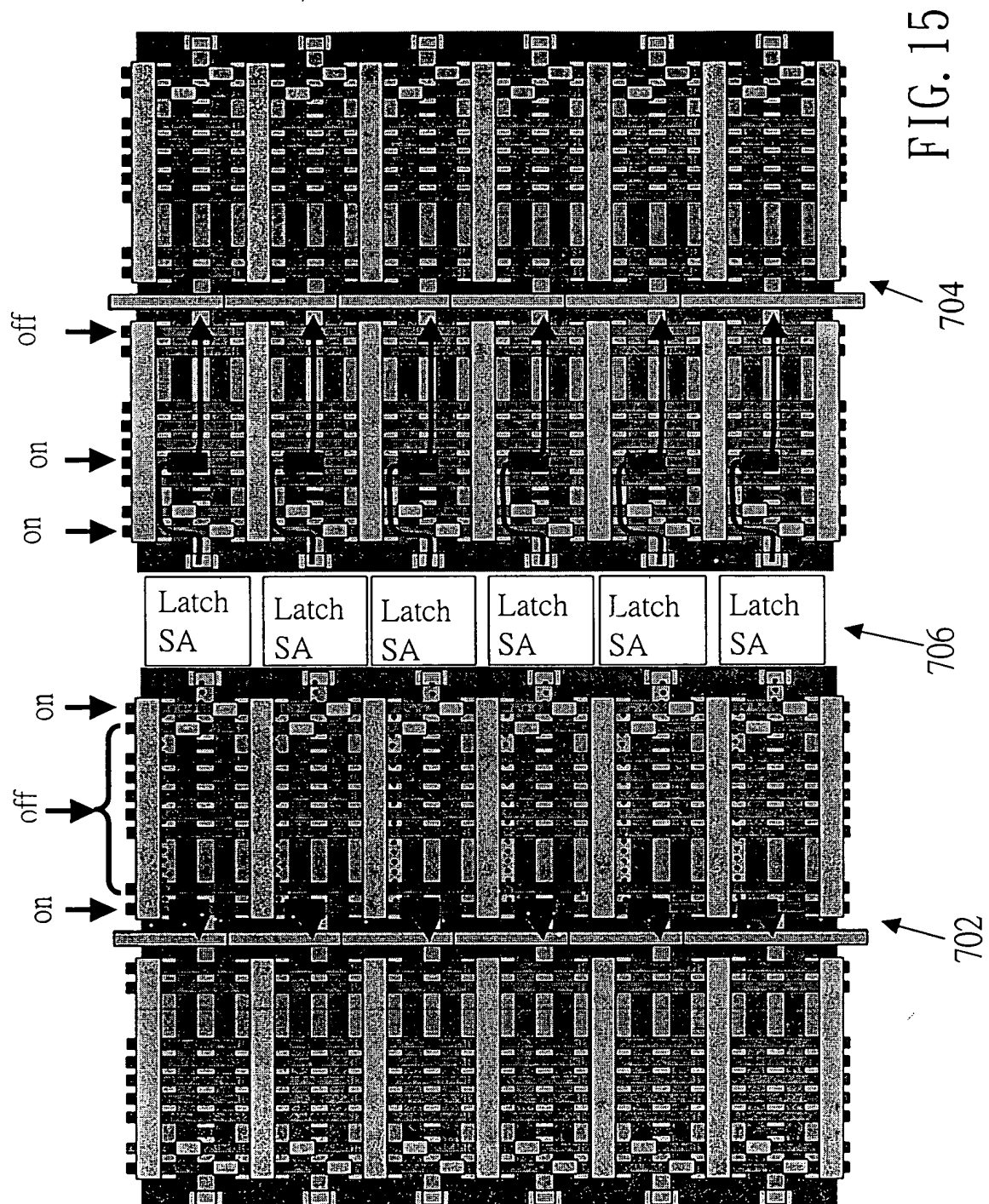


FIG. 14



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